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What is claimed is:

1. A method of driving a plasma display panel utilizing an asymmetry sustaining wherein the plasma display panel is divided into an upper block and a lower block for it's driving, said method comprising the steps of:

applying an upper driving signal for supplying a data to address electrode lines provided at the apper block; and

applying a lower driving signal for supplying a data to address electrode lines provided at the lower block in such a manner to overlap with the upper driving signal.

- 2. The method as claimed in claim 1, wherein the lower driving signal is applied at an approximately halftime of an application period of the upper driving signal.
 - 3. The method as claimed in claim 1, wherein a period when a period when the upper driving signal falls into a ground potential overlaps with a period when the lower driving signal remains at a stable voltage level.
 - 4. The method as claimed in claim 1, wherein a period when the lower driving signal falls into a ground potential overlaps with a period when the upper driving signal remains at a stable voltage level.
 - 5. The method as claimed in claim 3, wherein a data at the lower block is supplied at said period when the lower driving signal remains at a stable voltage level.
 - 6. The method as claimed in claim 4, wherein a data at the upper block is supplied at said period when the upper

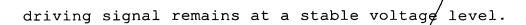
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7. The method as claimed in claim 1, further comprising the steps of:

driving an energy recovery circuit at said application time of said driving signals to raise said driving signals into a stable voltage level; and

driving the energy recovery circuit after said data was supplied to the corresponding block, thereby falling said driving signals into a ground voltage level.

- 8. The method as claimed in claim 7, wherein signals for driving the energy recovery circuit have a phase difference between the upper block and the lower block.
- 9. A driving apparatus for a plasma display panel utilizing an asymmetry sustaining wherein the plasma display panel is divided into an upper block and a lower block for it's driving said driving apparatus comprising:
- a first address driver for driving first address electrode lines included in the upper block;
- a second address driver for driving second address electrode lines included in the lower block; and
- control means for applying first and second control signals having a desired phase difference to control an energy recovery direct included in each of the first and second address drivers.
- 10. The driving apparatus as claimed in claim 9, wherein the control means includes:

controller for generating the first and second control signals and applying them to the first and second address drivers; and



- a delay, being provided between the controller and the second address driver, for delaying the second control signal.
- 5 11. The driving apparatus as claimed in claim 10, wherein the delay delays the second control signal such that a driving signal can be applied from the second address driver to the address electrode lines at an approximately half time of a driving signal applied from the first address driver to the address electrode lines.
 - 12. The driving apparatus as claimed in claim 9, further comprising:
 - a first scanning/sustaining driver for driving scanning/sustaining electrode lines included in the upper block;
 - a second scanning/sustaining driver for driving scanning/sustaining electrode lines included in the lower block; and
- 20 a common sustaining driver for driving common sustaining electrode lines included in the upper and lower blocks.